

1588 U.S. PTO  
09/772080



Class	Subclass	ISSUE CLASSIFICATION



PATENT NUMBER

# U.S. UTILITY Patent Application

O.I.P.E. SCANNED <i>TR</i> <i>PC3</i> o.a. <i>CH</i>	PATENT DATE
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CLASS <i>375</i>	SUBCLASS	ART UNIT <i>2631</i>	EXAMINER
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APPLICATION NO. 09/772080	CONT/PRIOR F	CLASS 375	SUBCLASS <i>371</i>	ART UNIT 2631	EXAMINER <i>PHU</i> <i>Burd</i>
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APPLICANTS

Nobutaka Taniguchi  
Hiroyoshi Tomita

TITLE

Delay time adjusting circuit comprising frequency dividers having different frequency division rates

PTO-2040  
12/89

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	_____ (Assistant Examiner)    _____ (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner)    _____ (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
	_____ (Legal Instruments Examiner)    _____ (Date)			<b>ISSUE BATCH NUMBER</b>	

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